

# Daev Mithran

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## Summary

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A passionate researcher and Full-Stack developer with an experience of 2 years in National Blockchain project. Working on opensource contributions in Hyperledger. My good understanding of distributed systems and Applied cryptography will make me an valuable asset to the team. I am actively looking for opportunities in web3

## Education

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### Indian Institute of Technology Hyderabad

BACHELOR OF TECHNOLOGY IN ENGINEERING PHYSICS

2016-2020

- GPA: 7.16/10.00

## Skills

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- Languages** C++, Python, Javascript, Solidity, Golang, Rust
- Blockchain** Hyperledger Fabric, Hyperledger Indy, Ethereum
- Development** ExpressJs, ReactJs, MongoDB, Node, Git, Vagrant, PostgreSQL

## Experience

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### National Blockchain Project, IIT Kanpur

SENIOR PROJECT EXECUTIVE ENGINEER

Sept,1st 2020 - Current, 1 year 8 months

- Blockchain and applied cryptography researcher
- Open source contributor in Hyperledger Acapy
- Full-Stack development using MERN

### CRUBN, IIT Kanpur

RESEARCH INTERN

May,4th 2020 - Aug ,4th 2020, 3 mos

- Research on blockchain interoperability in Hyperledger Fabric
- Implemented atomic swaps between Hyperledger Fabric and Ethereum chain
- Atomic swaps implemented was a new approach independent of time

### Redpine Signals Inc, Hyderabad

RESEARCH INTERN

Feb 1,2020 - May 31,2020, 4 mos

- Research on Decentralized Identity for IoT
- Generated source of identity using SRAM PUF

## Projects

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### E-voting using Verifiable Credentials

- Researched on Anonymous Credentials
- Proposed an architecture for e-voting which authenticates an user anonymously and allows to vote only once
- Implemented solution in Hyperledger Aries

### Blockchain Interoperability

- Researched on various Interoperability approaches
- Created an atomic swap solution for blockchains without timelocks
- Implemented solution in Hyperledger Fabric

### Decentralized identifiers for IoT

- Proposed an architecture to implement Decentralized Identity for IoT Devices
- The project aims to bind digital identity to IoT devices using PUFs